Annotated Sheet Showing Changes

5/11

FIG. 3

500

MAC-PHY INTERFACE

CONFIGURATION PORT

507 RX_D

RX_RDY

RX_ACTIVE
RX_START
TX_ACTIVE
TX_START

TX_RQST

TX_D 509

SPI

RESET

CLK

REF_CLK

503
802.11a,g OFDM Rx path
802.11b Rx path

524
TX/RX CONTROL

RADIO CONTROL
SOP Detector 522
ACG Controller 523
521

505
802.11a,g OFDM Tx path
802.11b Tx path

CNTRL/STATUS REGISTERS 527

RESETS

CLOCK GEN 531

REF. DISTRIBN 525

TEST 529

501 ADC
RX_OUT

511 RSSI ADC
RSSI-IF

513 RSSI ADC
RSSI-BB

CNTRL

515 CGI SIGNALS
517

TX_I DAC

TX_Q DAC

ADC_REFS

ADC & DCA BIAS

V_BIAS

RADIO INTERFACE